Assembly Language – IA-32 Processor Architecture (1)

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* 1. IA-32 Processor Architecture、Memory Management

1. Register EAX, EBX, ECX, and EDX are have 16 bit that can be considered as low position of register, which 16 bit can be divided into 8 bit each register. To use these register, we have to refer their name. Here the EDX register, please fill in the name of the low register.

|  |  | DH | DL |
| --- | --- | --- | --- |
|  |  | 8-bit | 8-bit |
|  | | DX | |
|  | | 16-bit | |
| EDX | | | |